

**S/N 08/960,405**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kevin J. Ryan

Examiner: Michael T. Tran

Serial No.: 08/960,405

Group Art Unit: 2818

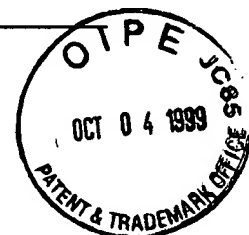
Filed: October 29, 1997

Docket: 303.638US3

Title: MEMORY HAVING A PLURALITY OF EXTERNAL CLOCK SIGNAL  
INPUTS

**NOTIFICATION OF CHANGE OF CORRESPONDENCE ADDRESS**

Assistant Commissioner for Patents  
Washington, D.C. 20231



Applicant respectfully requests that all future correspondence in the above-identified patent application be addressed to:

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Respectfully submitted,

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By

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Box Issue Fee, Assistant Commissioner of Patents, Washington, D.C. 20231 on September 28, 1999.

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